

What is claimed is:

1. A method for data transmission between an IO (input-output) card with an add-on memory card and a host system, comprising the steps of:
 - inserting the memory card in the IO card;
 - 5 connecting the IO card to the host system; and
 - generating a bus control signal through an IO controller on the IO card to switch to a first data bus or a second data bus located in the IO card thereby to change data transmitting path between the memory card and the host system.
- 10 2. The method of claim 1, wherein the bus control signal controls a switch to switch to the first data bus or the second data bus.
3. The method of claim 1, wherein the IO card and the memory card share all of the data buses of the IO card to transmit data with the host system when the memory card is connected to the first data bus to transmit data.
4. The method of claim 1, wherein the memory card and the IO card further transmit
15 data directly via the first bus without passing through the data bus of the host system.
5. The method of claim 1, wherein the memory card uses a portion of the data buses of the IO card to transmit data with the host system when the memory card is connected to the second data bus to transmit data, the IO card uses remaining data buses to transmit data with the host system.
- 20 6. The method of claim 1, wherein the IO card has a socket to electrically and mechanically couple with the memory card.
7. The method of claim 1, wherein the host system has an insertion slot to electrically and mechanically couple with the IO card.
8. A method for data transmission between an IO (input-output) card with an add-on

memory card and a host system, the memory card inserting in the IO card which is connected to the host system, the method comprising:

generating a bus control signal through an IO controller located in the IO card, thereby to alter a data transmitting path between the memory card and the host system
5 via switching the data transmitting path to a first data bus or a second data bus located in the IO card.

9. The method of claim 8, wherein the bus control signal controls a switch to switch to the first data bus or the second data bus.

10. The method of claim 8, wherein the memory card and the IO card further transmit
10 data directly via the first bus without passing through the data bus of the host system.

11. The method of claim 8, wherein the IO card and the memory card share all of the data buses of the IO card to transmit data with the host system when the memory card is connected to the first data bus to transmit data.

12. The method of claim 8, wherein the memory card uses a portion of the data buses of
15 the IO card to transmit data with the host system when the memory card is connected to the second data bus to transmit data, the IO card uses remaining data buses to transmit data with the host system.

13. The method of claim 8, wherein the IO card has a socket to electrically and mechanically couple with the memory card.

20 14. The method of claim 8, wherein the host system has an insertion slot to electrically and mechanically couple with the IO card.